



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : TSENG, SHIN-HSIEN Attorney Docket No. : 2001066
Serial No. : 10/712,318 Group Art Unit: 2814
Filed : 11/12/2003 Examiner: NATHAN W. HA
Title: INTEGRATED CIRCUIT DEVICE AND THE MANUFACTURING
METHOD THEREOF

Honorable Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

This paper is in response to the Official Action mailed June 1, 2005.
Applicants respectfully submit the following amendments and comments in
connection with the above-named application.